

*CLAIM AMENDMENTS*

R.B.  
2/22/05

*Previously Presented*

1. (Currently Amended) An operation circuit of an operation mechanism that includes first and second coils arranged so that a moving element may be driven between the coils, the operation circuit comprising means for suppressing an over-voltage upon interrupting an excitation current of the first coil and for interrupting an induction current generated through the first coil when the second coil is excited.
2. (Currently Amended) The operation circuit according to claim 1, wherein ~~the~~ means for suppressing is connected in parallel to ~~the~~ the first and second coils, and consists of diodes and induction interruption switches.
3. (Currently Amended) The operation circuit according to claim 1, wherein ~~the~~ means for suppressing is connected in parallel to ~~the~~ the first and second coils, and consists of capacitors and resistors.
4. (Previously Presented) The operation circuit according to claim 1, including coil excitation means, respective capacitors for each of the first and second coils, and a single charging circuit for all of the capacitors.
5. (Previously Presented) The operation circuit according to claim 1, including discharge switches turned ON in synchronization with or after turning ON induction interruption switches.
6. (Currently Amended) The operation circuit according to claim 2, including discharge switches turned ON in synchronization with or after turning ON the induction interruption switches.
7. (Previously Presented) The operation circuit according to claim 1, including induction interruption switches turned OFF after a predetermined time period has passed since excitation means of the first and second coils has turned OFF.
8. (Currently Amended) The operation circuit according to claim 2, ~~including~~ wherein the induction interruption switches are turned OFF after a predetermined time period has passed since excitation means of the first and second coils has turned OFF.